



FIG. 2

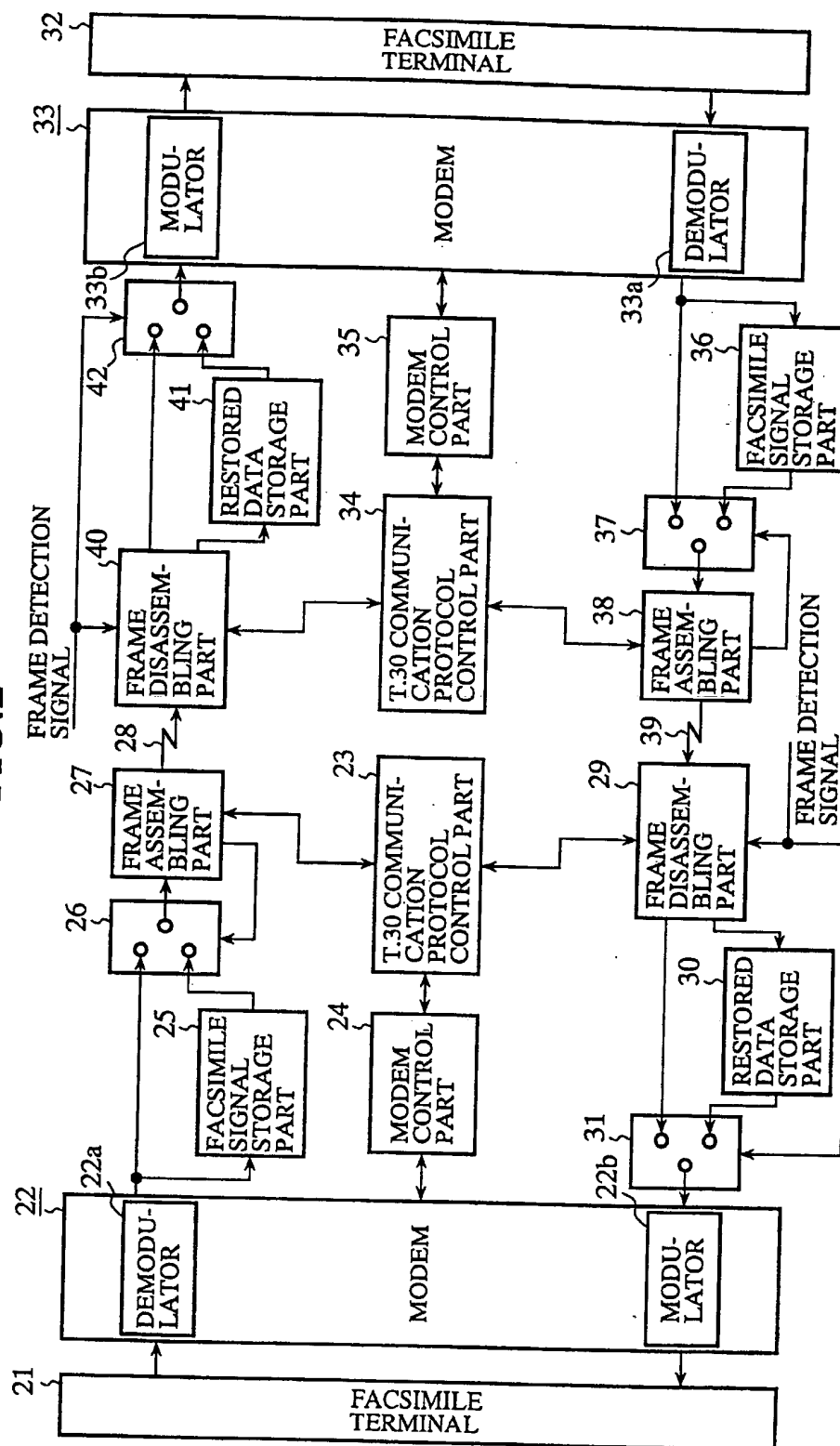


FIG.3

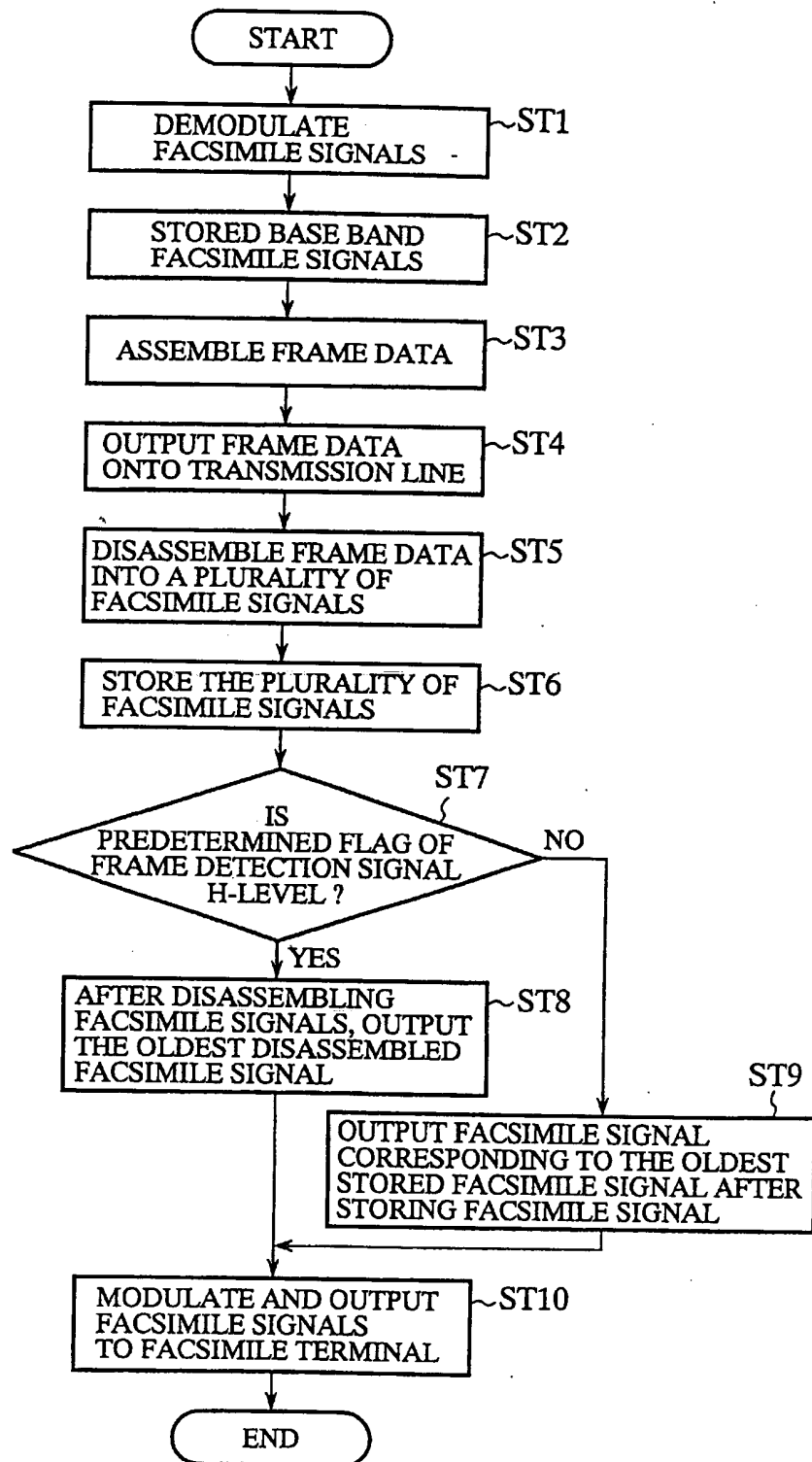


FIG. 4

FACSIMILE SIGNALS IN THE CYCLE BEFORE LAST		FACSIMILE SIGNALS IN THE PRECEDING CYCLE		CURRENT FACSIMILE SIGNALS	
1 FRAME	HEADER	NTH-MINUS-FOUR DATA	NTH-MINUS-THREE DATA	NTH-MINUS-TWO DATA	NTH-MINUS-TWO FRAME
	HEADER	NTH-MINUS-THREE DATA	NTH-MINUS-TWO DATA	NTH-MINUS-ONE DATA	NTH-MINUS-ONE FRAME
	HEADER	NTH-MINUS-TWO DATA	NTH-MINUS-ONE DATA	NTH DATA	NTH FRAME
	HEADER	NTH-MINUS-ONE DATA	NTH DATA	NTH-PLUS-ONE DATA	NTH-PLUS-ONE FRAME

FIG. 5

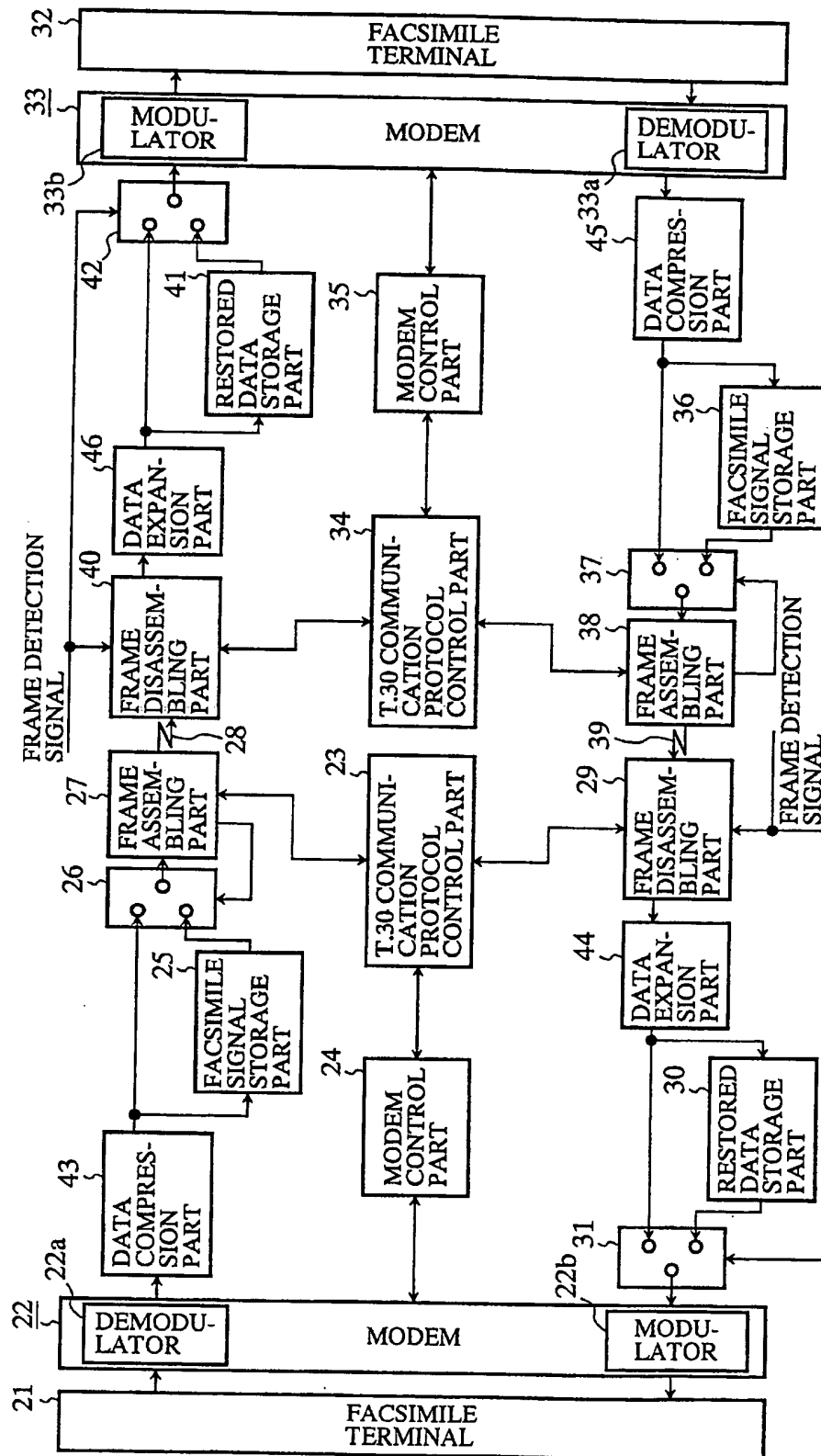


FIG. 6

1 FRAME	FACSIMILE SIGNALS IN THE PRECEDING CYCLE		CURRENT FACSIMILE SIGNALS	NTH-MINUS TWO FRAME NTH-MINUS ONE FRAME NTH FRAME NTH-PLUS ONE FRAME
	HEADER	n-3	n-2	
	HEADER	n-2	n-1	
	HEADER	n-1	n	
	HEADER	n	n+1	

FIG. 7

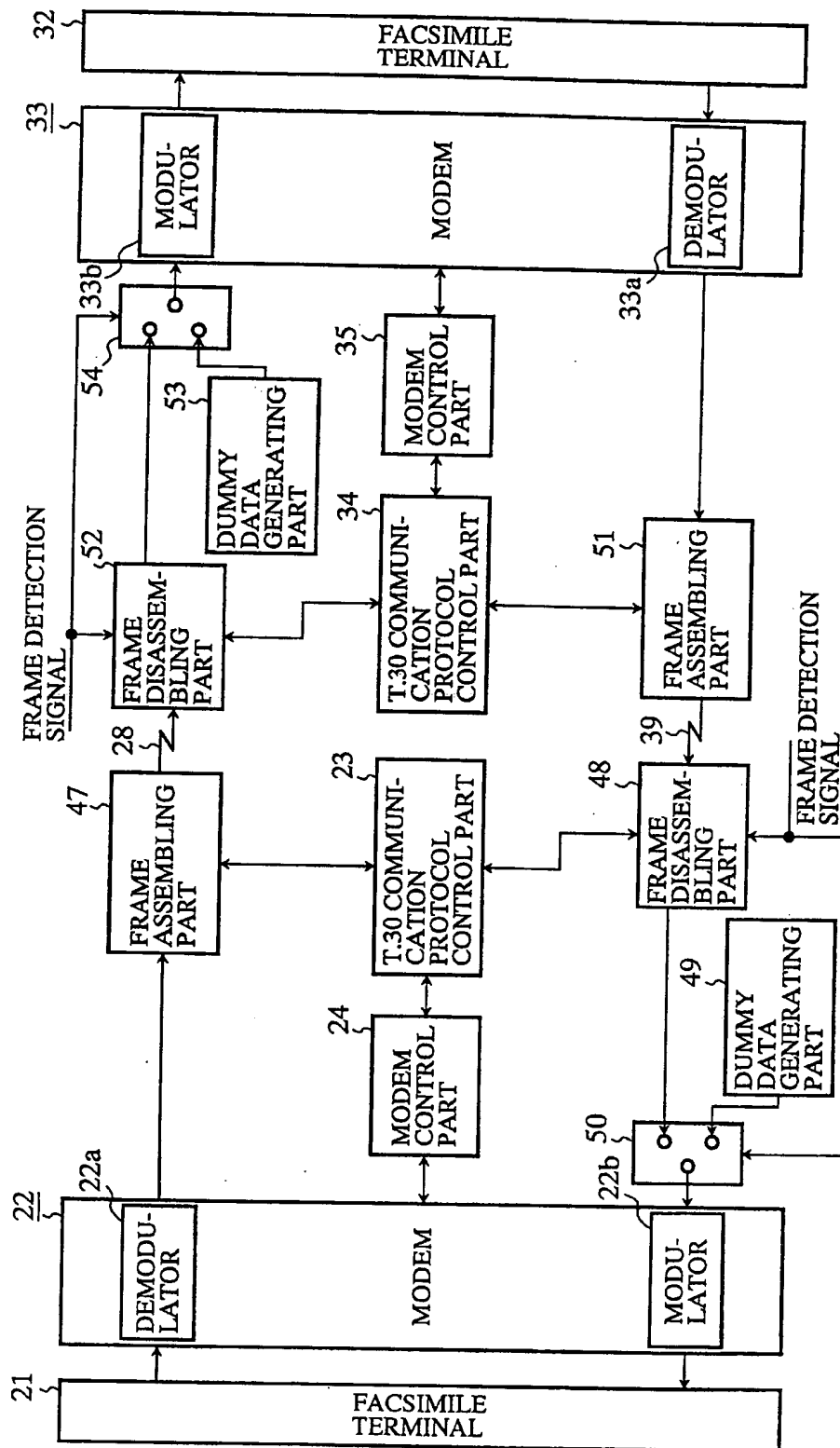


FIG. 8

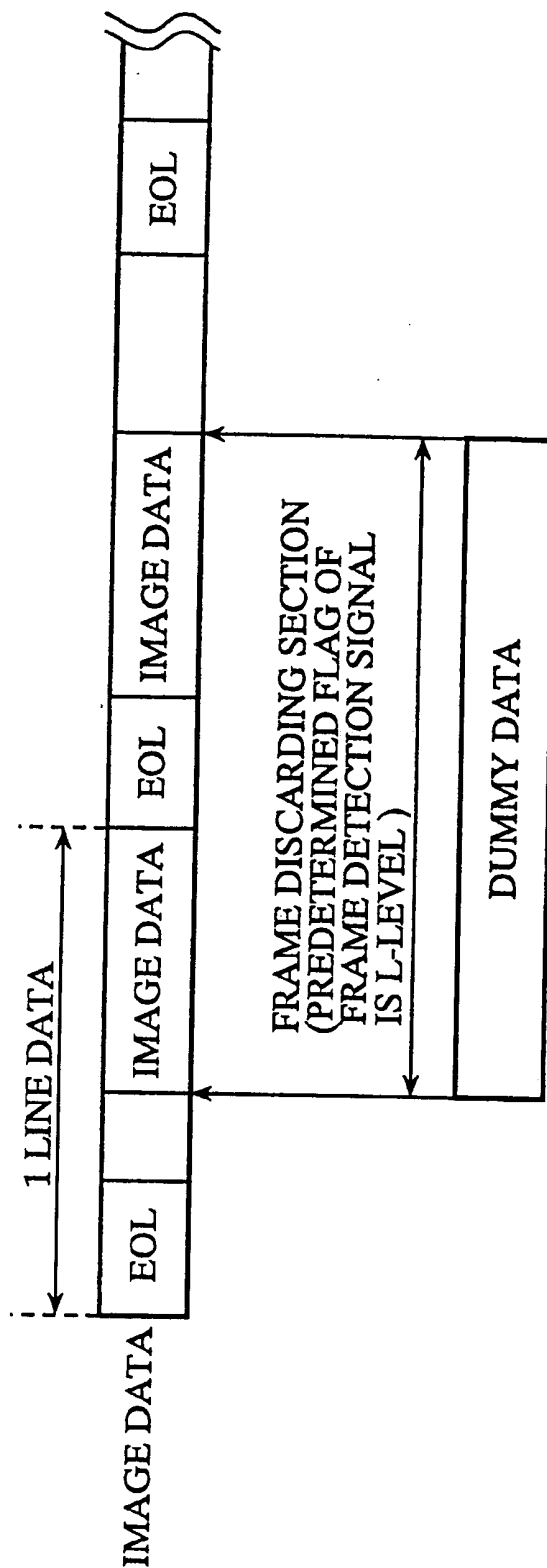




FIG. 9

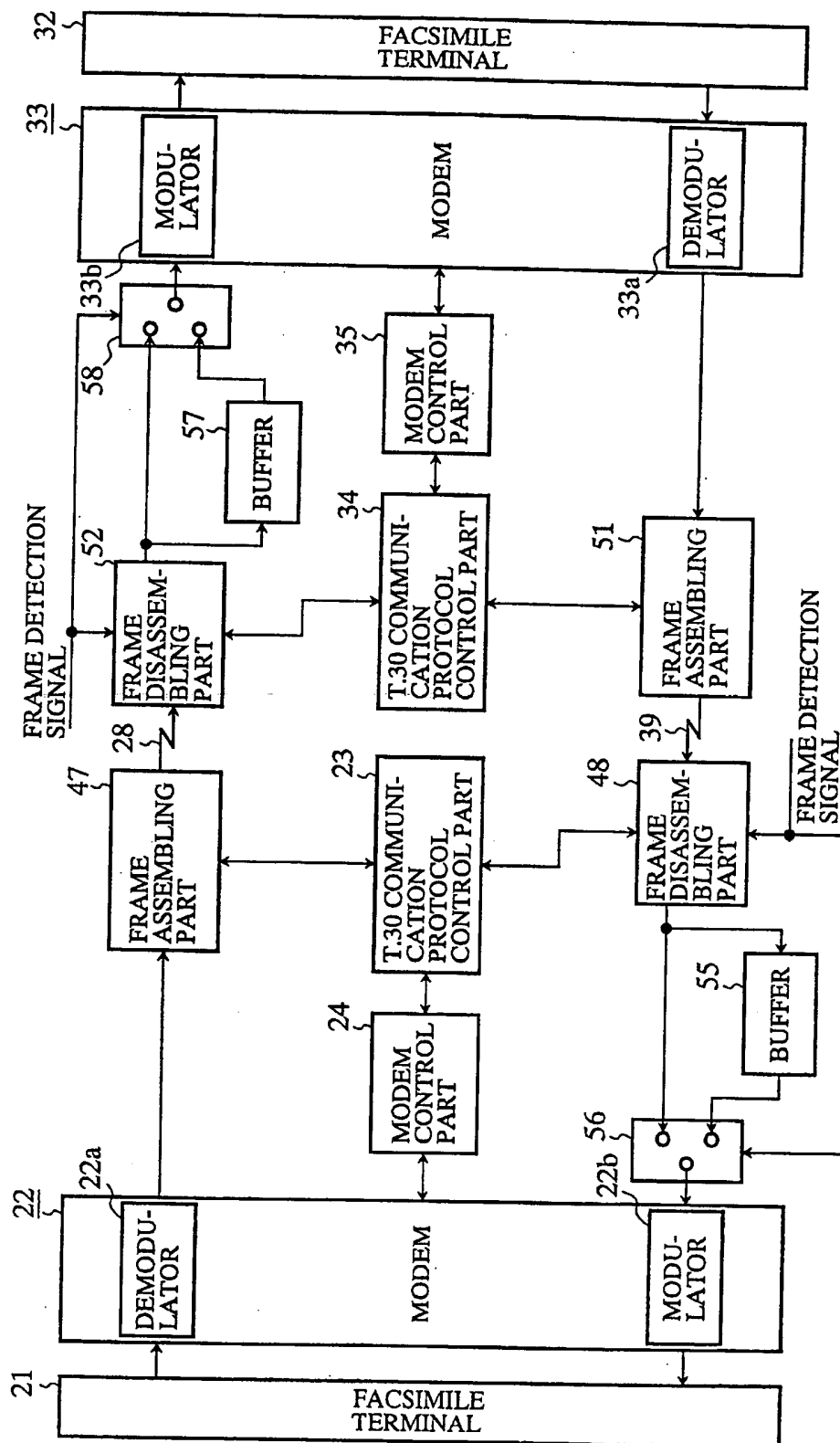


FIG. 10

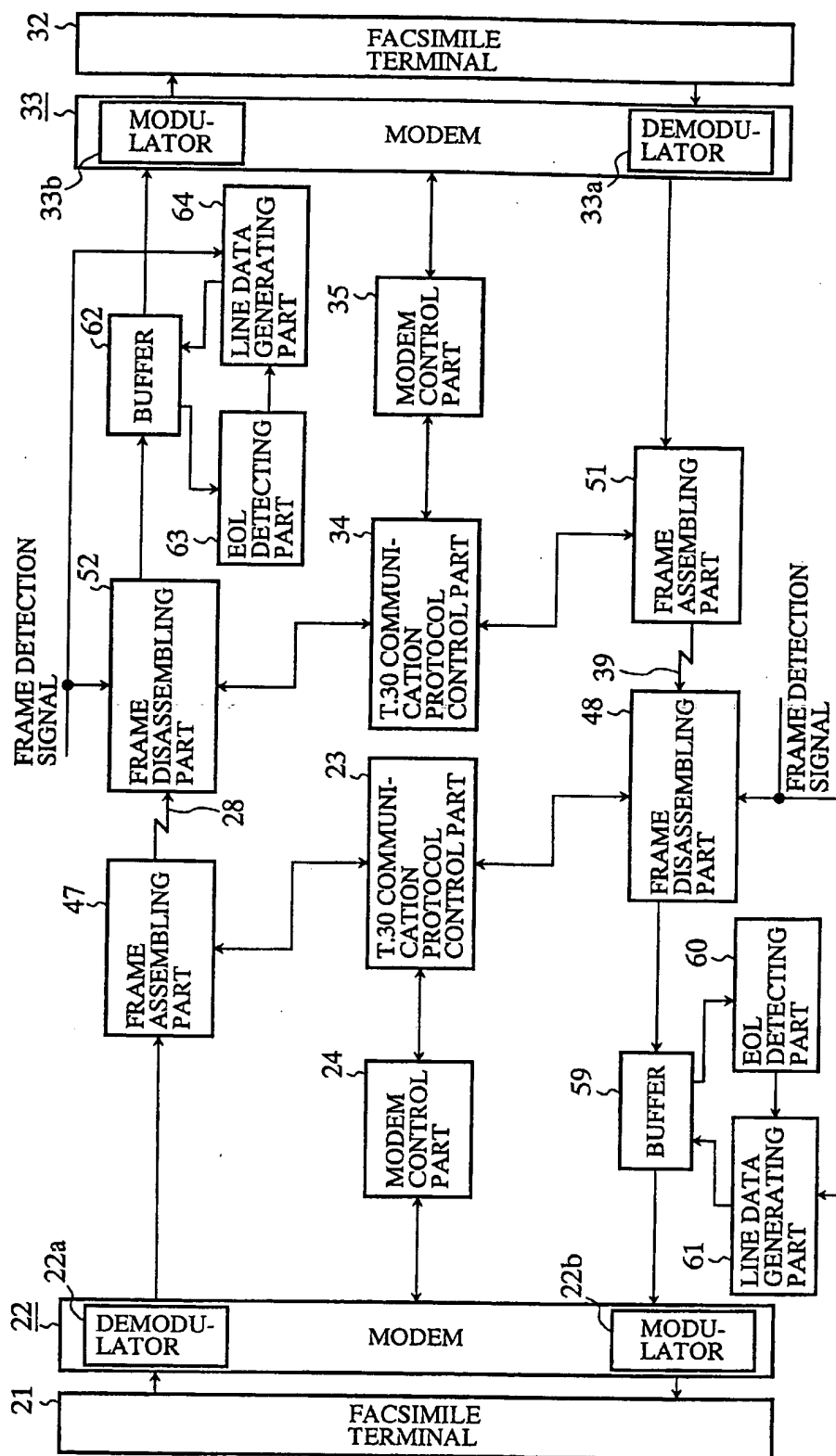


FIG. 11

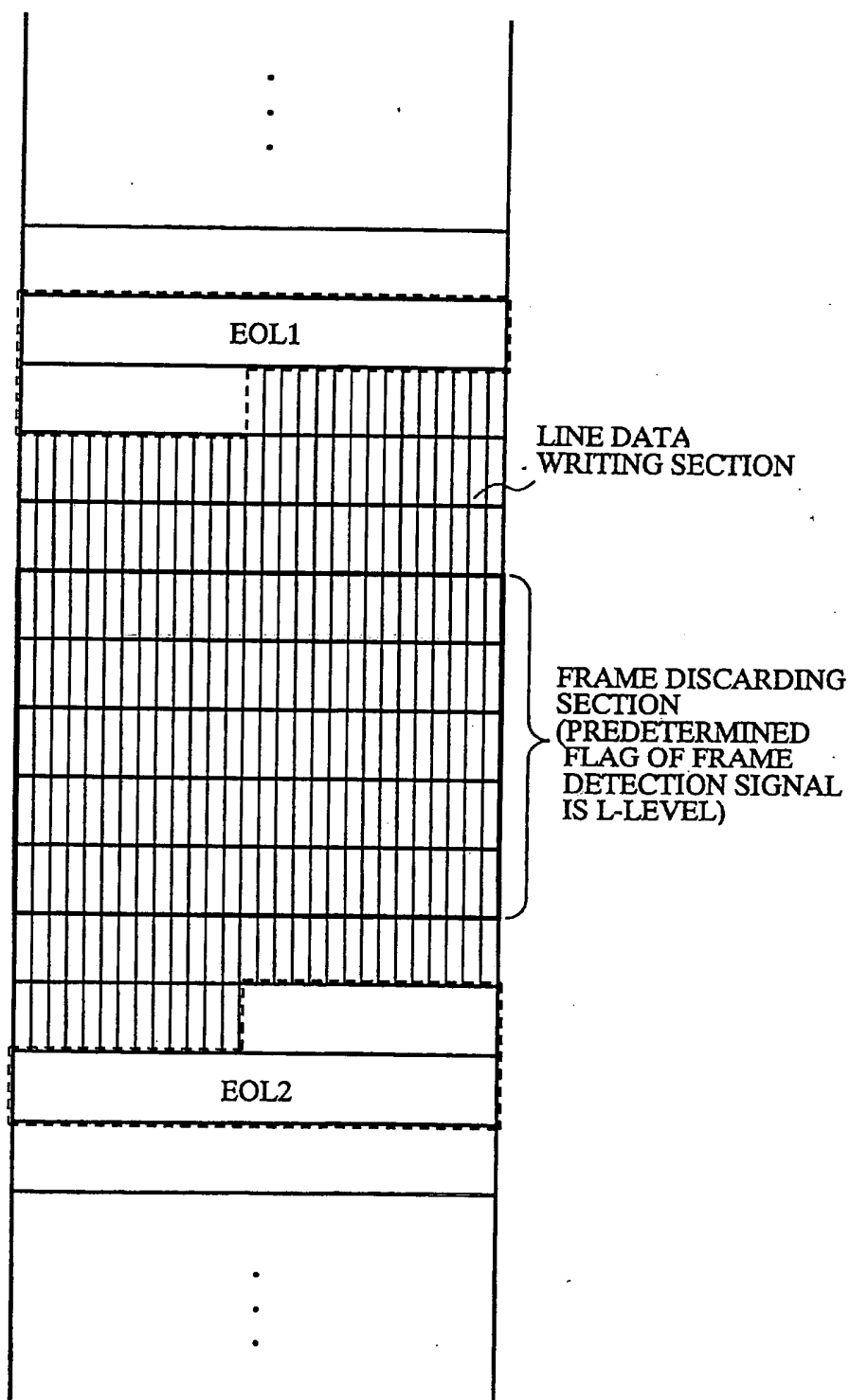


FIG. 12

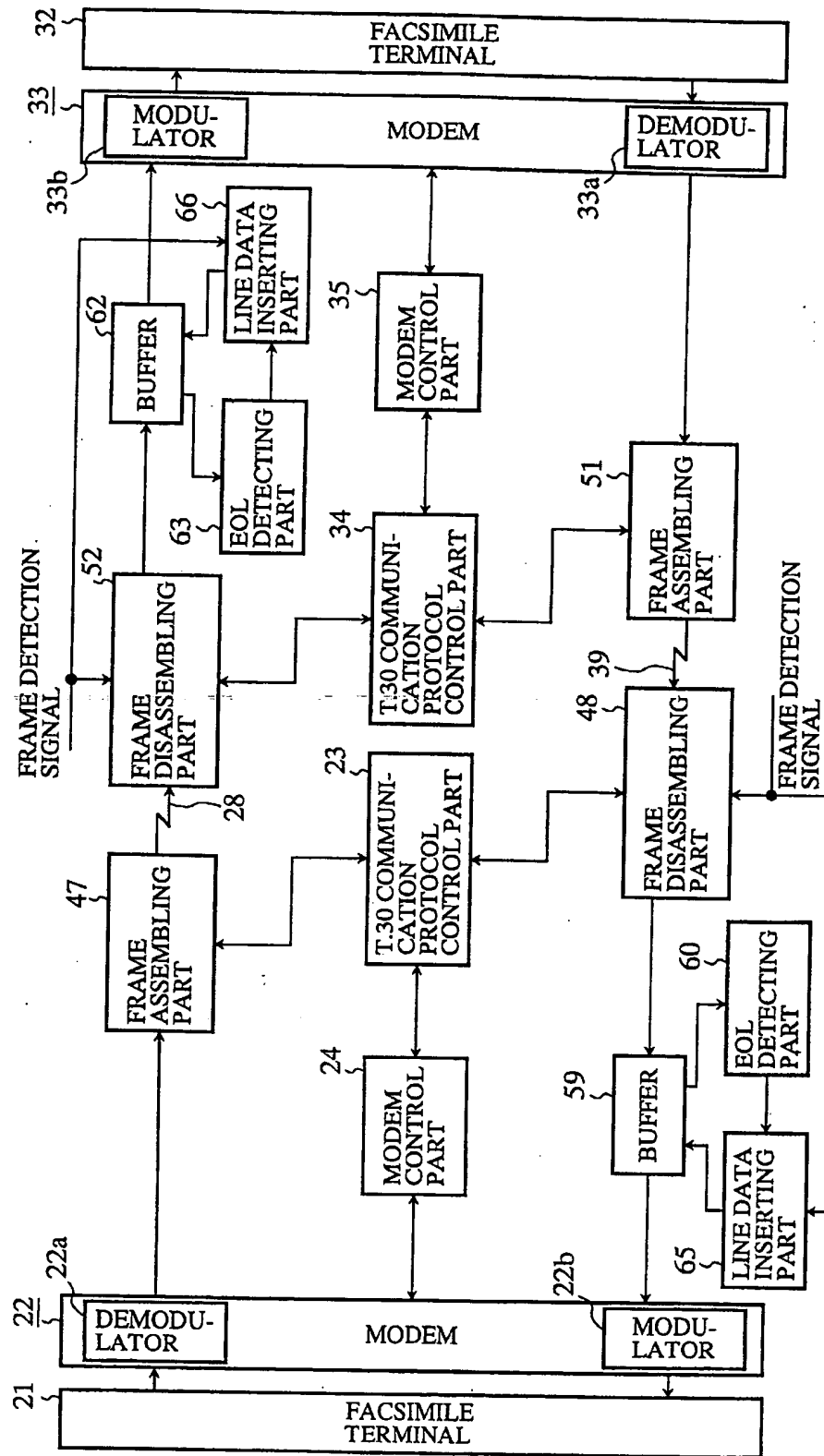


FIG.13

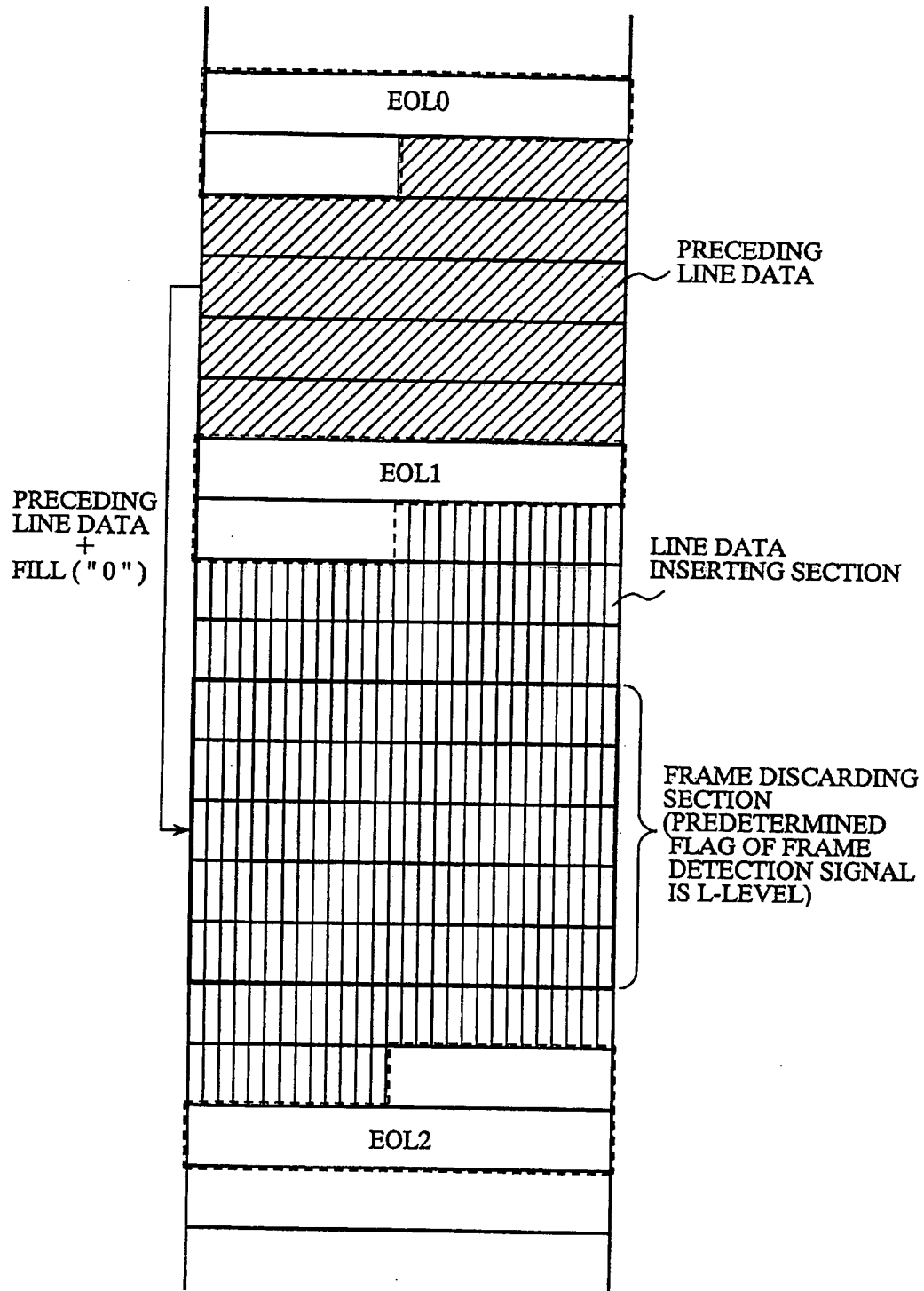


FIG. 14

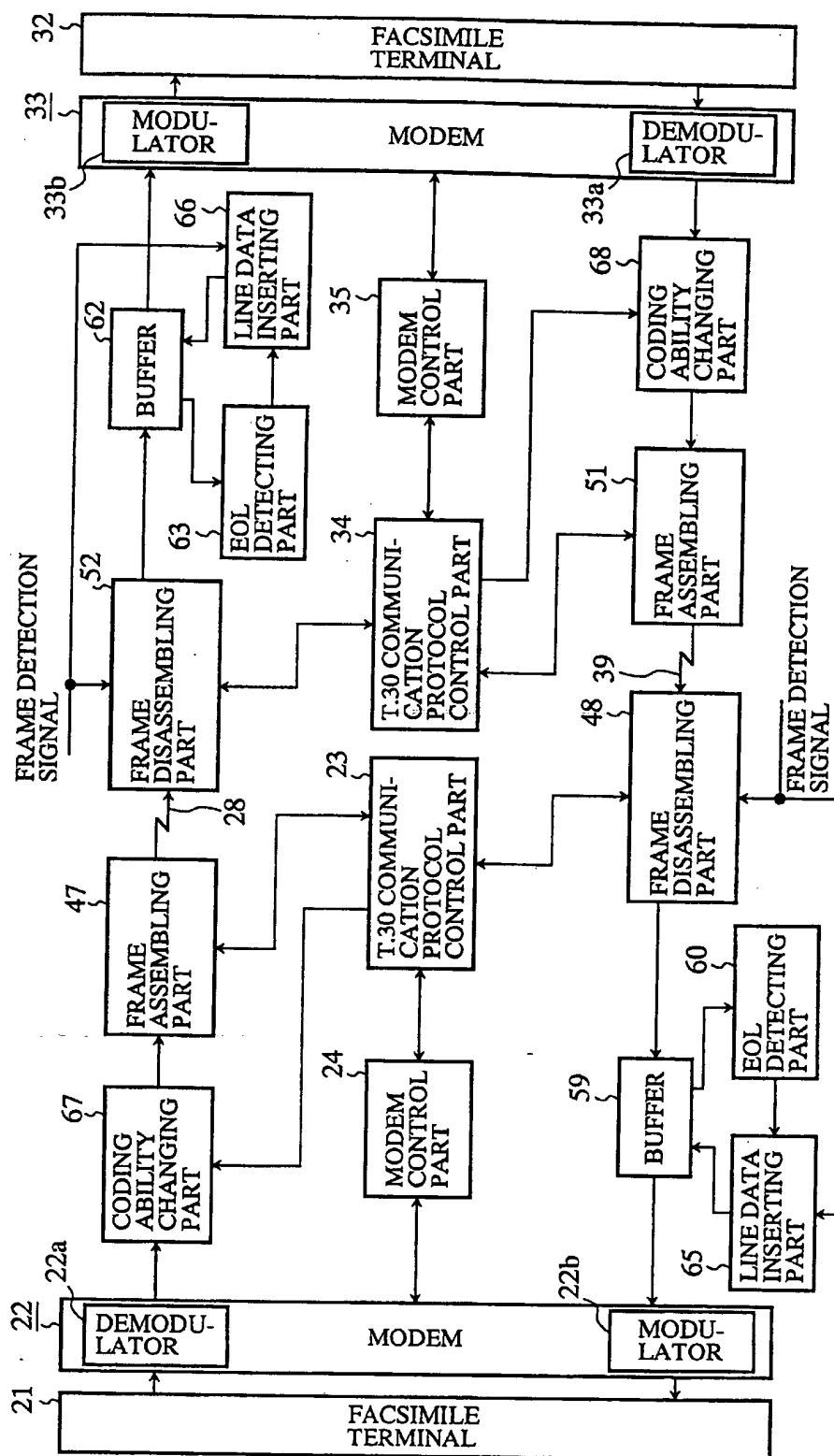


FIG. 15

